A THREE DAY WORKSHOP
ON
ADVANCED COMPUTER ARCHITECTURE

November 11th to 13th 2006

Convener
Mr. Bhagwat Prasad Chaudhury
E-mail : bhagwat@silicon.ac.in
Phone No. 9861067397 (Mob)

Organized by
Dept. of Computer Sc. & Engineering
Silicon Institute of Technology
Silicon Hills, Bhubaneswar-24
Ph. No.: (0674) 2725448 / 649
Website : www.silicon.ac.in

In collaboration with
Orissa Information Technology Society (OITS)
Introduction
“The best way to acquire knowledge is to share it” Silicon Institute of Technology (SIT) along with Orissa Information Technology Society (OITS) is organizing a 3 days workshop on “Advanced Computer Architecture” to keep pace with rapidly changing global scenario of software industry.

Objective
- To augment the knowledge of teachers on the subject.
- To enable faculties for pursuing research and development activities in this field.

Topics to be covered
- Input-Output organization
- Architectural classification of parallel processing
- Pipelining
- Array processors
- Multiprocessor

Resource Persons
1. Prof. (Dr.) Aurobinda Routray, IIT, Kharagpur
2. Prof. Biswajit Sahoo, KIIT, Deemed University, Bhubaneswar.
3. Prof. A. K. Das, CITE, Bhubaneswar.
4. Prof. Amiya Rath, KEC, Bhubaneswar.

Some more resource persons are expected to join.

Who can Participate
- Faculty members teaching / intending to teach architecture.
- Researchers intending to work in the field of architecture.

Registration details
Registration is mandatory for participation.
- Registration fees: Rs. 1,000/- per participant.
- Mode of payment: Cash / DD in favour of “Silicon Institute of Technology” payable at Bhubaneswar.

Venue
Silicon Campus, Patia, Bhubaneswar-751024.
Ph. No.: (0674) 2725448 / 649
About Silicon
Silicon marked its presence in the field of technical education with a difference. The campus is situated in a beautiful, landscaped green stretch of land at the footsteps of Silicon Hills in Patia. Silicon facilitates a teaching-learning process where education is revered and rewarded.

About OITS
Orissa Information Technology Society (OITS) was established in the year 1997 in the state of Orissa with the noble thought of bringing information technology to grass root level of the state. Since its inception it has proved its substance by organizing many workshops and other development programs in the field.

Advisory Committee
• Prof. B. Patnaik, Director Academics, Silicon Institute of Technology.
• Prof. S. Padhy, President, OITS
• Prof. A. K. Nayak, HOD, CSEA Dept., Silicon Institute of Technology

Arrangements
• Lodging and Boarding shall be available for the participants on prior information to the co-ordinator.
• Transport facility for the participants shall be available from city centers to the campus.

PROGRAMME SCHEDULE

Day 1 (11.11.2006)

<table>
<thead>
<tr>
<th>Time</th>
<th>Event</th>
</tr>
</thead>
<tbody>
<tr>
<td>09.00 - 10:00 am</td>
<td>Registration and Inauguration</td>
</tr>
<tr>
<td>10.00 - 10:15 am</td>
<td>Tea Break</td>
</tr>
<tr>
<td>10:15 - 12:15 pm</td>
<td>Lecture I (Module-I)</td>
</tr>
<tr>
<td>12:15 - 01:15 pm</td>
<td>Lecture II (Module-II)</td>
</tr>
<tr>
<td>01:15 - 02:15 pm</td>
<td>Lunch Break</td>
</tr>
<tr>
<td>02:15 - 04:15 pm</td>
<td>Lecture III (Module-I)</td>
</tr>
<tr>
<td>4:15 - 4:30 pm</td>
<td>High Tea</td>
</tr>
<tr>
<td>4:30 – 5:30 pm</td>
<td>Lecture IV (Module-II)</td>
</tr>
</tbody>
</table>

Day 2 (12.11.2006)

<table>
<thead>
<tr>
<th>Time</th>
<th>Event</th>
</tr>
</thead>
<tbody>
<tr>
<td>09:00 - 11:00 am</td>
<td>Lecture V (Module-III)</td>
</tr>
<tr>
<td>11:00 - 11:15 am</td>
<td>Tea Break</td>
</tr>
<tr>
<td>11:15 - 01:15 pm</td>
<td>Lecture VI (Module-II)</td>
</tr>
<tr>
<td>01:15 - 02:15 pm</td>
<td>Lunch Break</td>
</tr>
<tr>
<td>02:15 - 04:15 pm</td>
<td>Lecture VII (Module-IV)</td>
</tr>
<tr>
<td>04:15 – 04:30 pm</td>
<td>High Tea</td>
</tr>
<tr>
<td>04:30 – 05:30 pm</td>
<td>Lecture VIII (Module-II)</td>
</tr>
</tbody>
</table>
Day 3 (13.11.2006)

09:00 - 11:00 am  Lecture IX (Module-IV)
11:00 - 11:15 am  Tea Break
11:15 - 01:15 pm  Lecture X (Module-III)
01:15 - 02:15 pm  Lunch Break
02:15 - 04:15 pm  Lecture XI (Module-IV)
04:15 – 04:30 pm  High Tea
04:30 – 05:30 pm  Lecture XII (Module-III)
05:30 onwards   Valediction

********

REGISTRATION FORM
THREE DAYS WORKSHOP ON “ADVANCED COMPUTER ARCHITECTURE”
Please register the following participant(s)
Name & Designation:  1.____________________________________________________________________________
2.____________________________________________________________________________
3.____________________________________________________________________________
Address: _________________________________________________________________________________________
________________________________________________________________________________________
Telephone No: ______________________________________ Mobile :________________________________________
Fax:_________________________________E-mail:  ______________________________________________________
Enclosed DD for Rs............................ in favour of Silicon Institute of Technology payable at Bhubaneswar, towards attending workshop.
DD No. _______________________________________  dt. __________________________________
Drawn on _____________________________________ for Rs. _________________________________
Date:                  Signature